FPGA System on Modules & ODM Designs

i Wave
Global

Building on our core competency of embedded expertise since 1999, iWave boasts of an extensive portfolio of high-performance FPGA and SoC FPGA System on Modules and embedded computing platforms enabling customers in their product development journey.

Explore the latest System on Modules releases from iWave:

- iW-RainboW-G58M: Powered by Altera Agilex 5
- iW-RainboW-G43M: Powered by Altera Agilex 7
- iW-RainboW-G63M: Powered by AMD Versal Premium Adaptive SoC
- iW-RainboW-G57M: Powered by AMD Versal AI Edge/Prime Adaptive SoC

Complementing our System on Modules, iWave enables customers with extensive ODM design services which includes carrier board design, system design, power and signal integrity analysis, and all the way to qualifications, certifications, production, and product lifecycle management.

A wide range of proven, optimized, and easy-to-use FPGA IP Cores along with reference designs to complement & quicken the development of your application. Our comprehensive suite of IP Cores includes ARINC 818 complete suite, storage, networking/bridge IP, and legacy x86 processors and their peripherals for all key markets and applications. The iWave IP cores provide the standard interface that can be easily integrated into FPGA devices from leading FPGA Silicon vendors like AMD, Altera, and Microchip.

Upcoming Events

Catch iWave in Action at the FPGA Conference Europe 2024

The FPGA Conference Europe is Europe's leading specialist conference for FPGA and programmable logic devices. iWave is excited to be a sponsor of the conference and experience some interesting demos. Register and participate in the conferences and technical sessions. Register now with code "**FPGA24_MEETUS**" for a **20% discount** on Guest tickets: https://rb.gy/nvwg33

Learn More

AMD Adaptive Computing Summit 2024
Boston || Los Angeles || San Jose || London

iWave is excited to showcase the Versal AI Edge System on Module and Versal Premium System on Module at the AMD Adaptive Computing summit. Catch us as we travel for the AMD Summit in Los Angeles, San Jose, and London.

To learn more about the event and reserve your spot, write to us at mktg@iwave-global.com.

Unveiling SoMs Powered by Versal AI Edge & Versal Premium SoC FPGAs

---

Live Webinar

Enabling Storage, Space, and Avionics Applications with iWave IP Cores on Microchip FPGAs

Last call to register! Join iWave for a technical deep dive into our storage and ARINC IP Core offerings, designed for seamless integration into Microchip FPGAs.

Here's what you will learn:

- ARINC 818 & ARINC 664 IP Cores
- Storage IP Cores – Features, Use Cases and Case Studies
- An overview of our FPGA and ODM Expertise

Date: May 9th, 2024
Time: 09:00 AM MST | 09:30 PM IST

Claim Your Spot

Latest FPGA Resources from iWave

* Article: Designing Radio Telescope Receivers Using RFSoC System on Module
* Article: Bring RoCE Capabilities to Zynq UltraScale+ MPSoC Development Kit
* Case Study: PCIe Switch System on Module for High-Speed Data Storage
* Case Study: ARINC 818-2 Single Channel Converter for Cockpit Video Display