



## Virtex Ultrascale+ FPGA SOM



Virtex Ultrascale+ FPGA System On Module supports Virtex Ultrascale+ FPGA and ARM Cortex A7 processor. The SOM features FPGA with programmable logic cells up to 3780K and Power-efficient 32-bit Dual Arm Cortex-A7 processor running up to 1.2 GHz.

The SOM is compatible with VU13P, VU11P, VU9P, VU7P & VU5P Virtex Ultrascale+FPGAs and VU160, VU190, VU125, VU080 & VU095 Virtex Ultrascale FPGAs in B2104 package. The module is capable of high speed connectivity peripherals such as 100G Ethernet, PCle, USB3.0, SATA 3.1, Gigabit Ethernet and dual 64-bit DDR4 with ECC and 32-bit DDR4 with ECC.

#### **APPLICATIONS:**

Data Centre Network Acceleration, Industrial IoT, Cloud Computing

# iW-RainboW-G47M HIGHLIGHTS

Xilinx Virtex Ultrascale+ FPGA with B2104 package

Dual 4GB FPGA-DDR4 with ECC (64bit + 8bit)

48 channels of GTY transceivers up to 32Gbps

Two 240pin High-Speed Connectors with 172 user IOs

Dual ARM Cortex-A7 core processor of 1.2GHz speed

2GB DDR4 for CPU with ECC (32bit+4bit)

QorlQ Trust Architecture and Arm TrustZone

4 lanes of 6Gbps SERDES from CPU

Industrial Grade Availability

#### **SPECIFICATIONS**

Virtex	UltraScale	+ FPGA

Virtex Ultrascale+ with B2104 package

Upto 3,780K Logic Cells

Upto 12,288 DSP Slices

#### CPU

Dual ARM Cortex-A7 core processor

Operation frequency of 1.2GHz

QorlQ Trust Architecture and Arm TrustZone

High-speed serial interfaces (SERDES) x4 @6Gbps

**Memory Interfaces** 

#### From FPGA

Dual 64-bit DDR4 with ECC

128MB QSPI flash

#### From CPU

32bit DDR4 with ECC

256MB parallel NOR Flash

4MB MRAM

512KB SRAM via SPI

#### **On SOM Features**

10/100/1000 Ethernet PHY

Temperature Sensor

TPM2.0 Module via SPI

**Quad Board to Board Connector interfaces** 

From FPGA

GTY Transceiver x48 up to 32Gbps

46 LVDS Pairs

80 SE FPGA IOs From CPU

SERDES x3 @6Gbps (PCIe, SATA, SGMII)

Gigabit Ethernet x 1

USB 2.0 OTG x 1

USB 3.0 x1

12C x 2

Debug UART

UART x 2

JTAG

**Operating Systems** 

Linux

**Power Supply** 

5V through Board to Board connector

**Temperature Support** 

-40°C to +85°C (Industrial grade operation)

**Form Factor** 

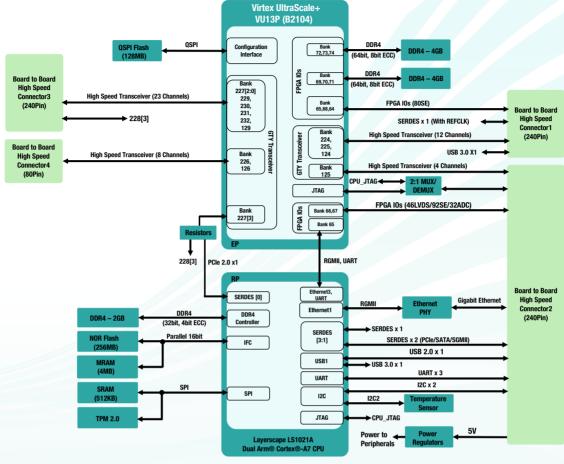
75mm x 110mm

**Environmental Specifications** 

REACH & RoHS3



### Virtex Ultrascale+ FPGA SOM Block Diagram



Compatible FPGAs:
VU13P, VU11P, VU9P, VU7P & VU5P Virtex Ultrascale + FPGAs and VU190, VU160, VU125, VU080 & VU095 Virtex Ultrascale FPGAs. Also compatible with KU19P, KU115 & KU095 VIV. VU19P, VU7P & VU5P Virtex Ultrascale + FPGAs and VU190, VU160, VU125, VU080 & VU095 Virtex Ultrascale FPGAs. Also compatible with KU19P, KU115 & KU095 VIV. VU19P, VU7P & VU5P VIV. VU5P VIV.

Bank Numbers mentioned in the block diagram is as per VU13P. Bank Number & Performance may vary for other compatible FPGAs Transceiver speed is limited to 16Gbps for Transceivers in Board to Board connector & 2 because of connector speed limit.

#### **OS SUPPORT**

Linux

#### **DELIVERABLES**

Virtex UltraScale + FPGA Module **Board Support Package** Example FPGA Design **User Manuals** 

#### **OPTIONAL KITS/Modules**

Virtex UltraScale+ Development Kit **Heat Spreader** 

#### **CUSTOM DEVELOPMENT**

BSP Development/OS Porting **Custom SOM/Carrier Development** Custom Application/GUI Development **Design Review and Support** 

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\*Optional items not included in the standard deliverables.

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#### Virtex Ultrascale+ FPGA SOM

The device can be ordered online from the iWave Website https://www.iwavesystems.com/product/virtex-ultrascale-fpga-som/ Or from our Local Partners in your region

http://www.iwavesystems.com/about-us/business-partner.html

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