

iW -sFPDP IP Core is based on ANSI/VITA 17.1-2015 Standards. This IP core provides a relatively simple protocol using maximum available data throughput with a minimum protocol for point-to-point data links. sFPDP links support a wide range of physical interfaces with the most common option being 10 gigabaud per second multimode fiber.

Applications

- Used in High-speed real time data transfer
- Applications includes modern day communication systems and Radar and signal processing system

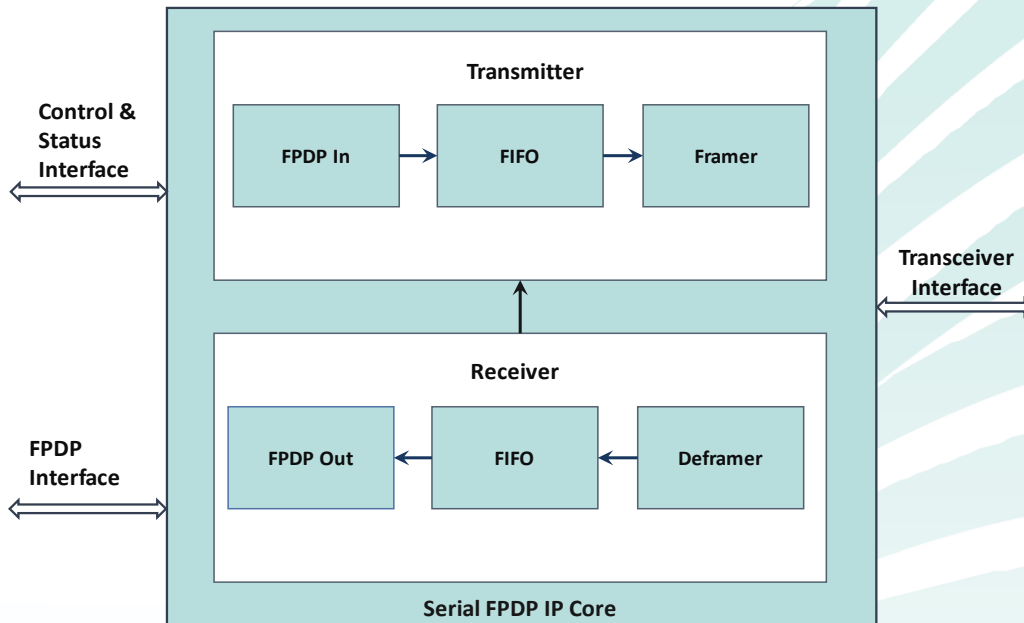
Highlights

- Serial Front Panel Data Port (sFPDP) IP core for FPGA is based on the ANSI/VITA 17.1-2015 standard.
- The Serial FPDP standard supports Up to 10Gbaud link speed.

Features

- Compliant with ANSI/VITA 17.1-2015 Serial FPDP standard
- Supported link speeds
 - o Up to 10Gbaud
- Data Frames supported
 - o Unframed Data
 - o Single Frame Data
 - o Fixed Size Repeating Frame Data
 - o Dynamic Size Repeating Frame Data
- System Configurations supported
 - o Basic System Flow Control
 - o Bi-directional Data Flow
 - o Copy Mode
 - o Copy/Loop Mode
 - o Unidirectional support
 - o Optional flow control
 - o CRC
- Host-Bus interface
 - o Parallel FPDP
- Configurable parameters Transmit FIFO depth
 - o Receive FIFO depth
 - o Transmit FIFO watermark to assert SUSPEND output
 - o Transmit FIFO watermark for TX FIFO Overflow signal generation
- Receive FIFO watermark for STOP/GO signal generation

iW- Serial FPDP block diagram



Deliverables

- RTL source code or Netlist
- IP example design
- IP datasheet
- Integration Manual
- Simulation Environment

Licensing Options

- Non-Transferable: Single Project/Product Netlist License – Single Site or Multi Site
- Non-Transferable: Multi Project /Product Netlist License – Single Site or Multi Site
- Non-Transferable: Single Project/Product RTL Source Code License – Single Site or Multi Site
- Non-Transferable: Multi Project/Product RTL Source Code License – Single Site or Multi Site

Technical Support

iWave provides comprehensive support during your system integration & validation.

- The Client may open a new support incident by emailing to a technical support engineer
- iWave's response time shall be within 24 hours of the initial call, with the details of the action plan to resolve
- Support assistance shall be delivered by telephone, email and/or remote assistance via a web meeting
- iWave shall provide remote debugging support irrespective of the time zone/ region

iWave Systems, a leading FPGA design house enhances your design productivity by providing an extensive suite of proven, optimized and easy-to-use FPGA IP Cores along with reference designs to complement and quicken your applications development. Our extensive suite of IP Cores covers all key markets and applications. Along with the rich set of FPGA IP cores, iWave offers custom FPGA designs tailored to meet the client specifications which includes RTL Design, Integration of iWave's or 3rd Party IP Cores on our FPGA SOMs with Carrier Card/ Custom Hardware/ Off-the-Shelf Evaluation Kits to provide end-to-end solutions targeting Low-Power, High-Performance and Optimized Designs

iW - Serial FPDP FPGA IP

The IP can be ordered online from the iWave Website <http://www.iwavesystems.com/product/serial-fpdp-sfpdp/>
Or from our Local Partners in your region <http://www.iwavesystems.com/about-us/business-partner.html>

INDIA

iWave Systems Tech. Pvt. Ltd.,
7/B, 29th Main, BTM Layout
2nd Stage, Bangalore-560076
Email: mktg@iwavesystems.com

JAPAN

iWave Japan, Inc.
8F-B, Kannai Sumiyoshi Building,
3-29, Sumiyoshi-cho, Naka-ku,
Yokohama, Kanagawa, Japan.
mktg@iwavesystems.com

EUROPE

International Sales & Marketing Europe
Venkelbaan 55 2908KE Capelle
aan den IJssel
The Netherlands
info@iwavesystems.eu

USA

iWave USA
1692 Westmont Ave.,
Campbell, CA95008
USA
info@iwavesystems.us