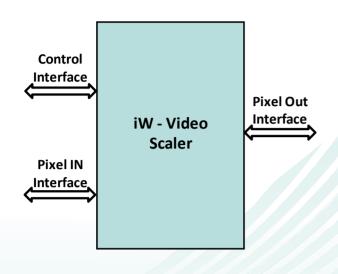


FPGA IP CORE iW- Video Scaler IP



iW - Video Scaler converts the video signals from one resolution to another resolution. This core can be used to convert low resolution to high or high resolution to low.

Applications

Can be used in all sorts of applications which use LCD,
Plasma, Digital Light Processing and etc.

Highlights

• Core uses 5-tap interpolation algorithm to implement the image resize.

Features

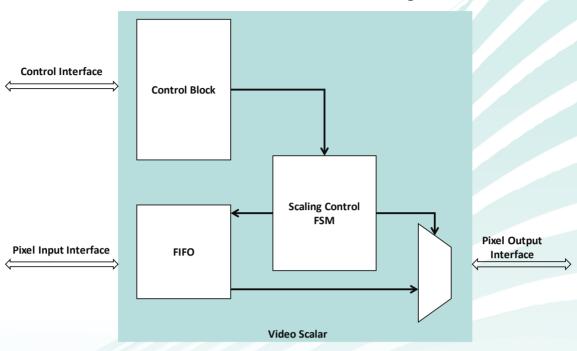
- Scaling factor: downscale/upscale by min 1 to max 4 independently in vertical and horizontal direction
- Programmable non-integer scaling factor
- Algorithm: 5-tap interpolating filter
- Quantization Level: Inter Pixel = 6, Filter Coefficients = 12
- Coefficients are software programmable
- Image size programmable from QCIF to Full HD
- Video Format: 4:4:4 RGB/YCbCr
- 8-bit per pixel per color
- 3 color plane in parallel processing

Core Benefits

- Can be combined with an ADC or digitizer and a DAC to support analog inputs and outputs
- Can be used to increase the resolution of video signals



iW - Video Scaler IP block diagram



Deliverables

- RTL source code or Netlist
- IP example design
- IP datasheet
- Integration Manual

Licensing Options

- Non-Transferable: Single Project/Product Netlist License Single Site or Multi Site
- Non-Transferable: Multi Project /Product Netlist License Single Site or Multi Site
- Non-Transferable: Single Project/Product RTL Source Code License – Single Site or Multi Site
- Non-Transferable: Multi Project/Product RTL Source Code License – Single Site or Multi Site

Technical Support

iWave provides comprehensive support during your system integration & validation.

- The Client may open a new support incident by emailing to a technical support engineer
- iWave's response time shall be within 24 hours of the initial call, with the details of the action plan to resolve
- Support assistance shall be delivered by telephone, email and/or remote assistance via a web meeting
- iWave shall provide remote debugging support irrespective of the time zone/ region

iWave Systems, a leading FPGA design house enhances your design productivity by providing an extensive suite of proven, optimized and easy-to-use FPGA IP Cores along with reference designs to complement and quicken your applications development. Our extensive suite of IP Cores covers all key markets and applications. Along with the rich set of FPGA IP cores, iWave offers custom FPGA designs tailored to meet the client specifications which includes RTL Design, Integration of iWave's or 3rd Party IP Cores on our FPGA SOMs with Carrier Card/ Custom Hardware/ Off-the-Shelf Evaluation Kits to provide end-to-end solutions targeting Low-Power, High-Performance and Optimized Designs

iW - Video Scaler FPGA IP

The IP can be ordered online from the iWave Website http://www.iwavesystems.com/product/video-scaler/ Or from our Local Partners in your region http://www.iwavesystems.com/about-us/business-partner.html

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