![iWave - Embedding Intelligence]

System Interface

Pixel Out Interface

iW - Video DeInterlacer

Pixel IN Interface

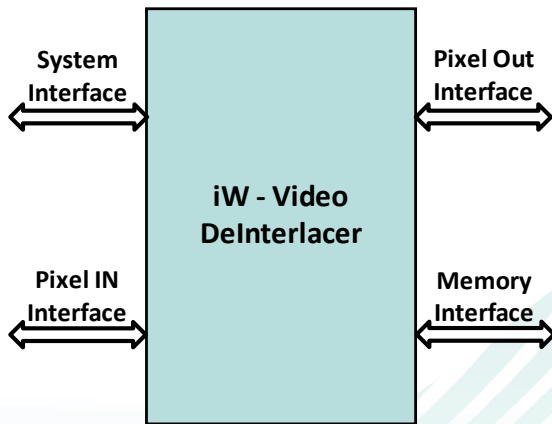**Memory Interface**

Video De-Interlacer converts the interlaced video signals into non-interlaced signals. This core separates the intermeshed odd and even fields of the NTSC/PAL frames output by the video decoder, into a combined, sequential de-interlaced frame, in order to display on LCD.

Highlights

• Core uses Weave method of video de-interlacing method

Features

• Weave Method of Video de-interlacing is supported

• Supports PAL or NTSC video mode

• RAM like memory interface with separate read and write controls
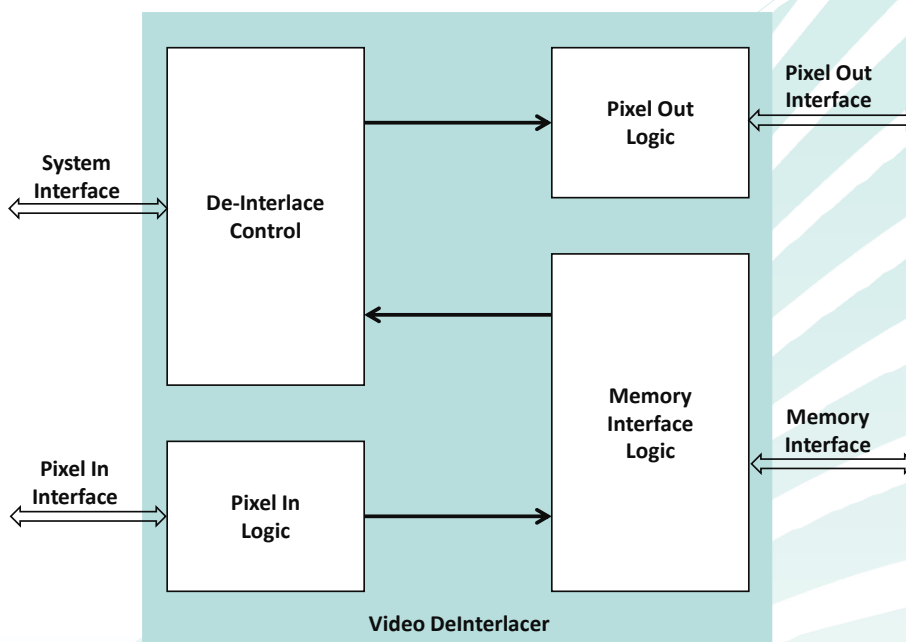
• Fully stable interfaces on both input and output

Applications

• Can be used in all kind of applications which use LCD

 display

• Can be used in image processing equipment's

Core Benefits

• Removes the flicker in the video signal without taking additional bandwidth

• Improves picture quality

iW - Video DeInterlacer IP block diagram

Video DeInterlacer

Deliverables

- RTL source code or Netlist
- IP example design
- IP datasheet
- Integration Manual

Licensing Options

- Non-Transferable: Single Project/Product Netlist License – Single Site or Multi Site
- Non-Transferable: Multi Project /Product Netlist License – Single Site or Multi Site
- Non-Transferable: Single Project/Product RTL Source Code License – Single Site or Multi Site
- Non-Transferable: Multi Project/Product RTL Source Code License – Single Site or Multi Site

Technical Support

iWave provides comprehensive support during your system integration & validation.

- The Client may open a new support incident by emailing to a technical support engineer
- iWave's response time shall be within 24 hours of the initial call, with the details of the action plan to resolve
- Support assistance shall be delivered by telephone, email and/or remote assistance via a web meeting
- iWave shall provide remote debugging support irrespective of the time zone/ region

iWave Systems, a leading FPGA design house enhances your design productivity by providing an extensive suite of proven, optimized and easy-to-use FPGA IP Cores along with reference designs to complement and quicken your applications development. Our extensive suite of IP Cores covers all key markets and applications. Along with the rich set of FPGA IP cores, iWave offers custom FPGA designs tailored to meet the client specifications which includes RTL Design, Integration of iWave's or 3rd Party IP Cores on our FPGA SOMs with Carrier Card/ Custom Hardware/ Off-the-Shelf Evaluation Kits to provide end-to-end solutions targeting Low-Power, High-Performance and Optimized Designs

iW - Video DeInterlacer FPGA IP
The IP can be ordered online from the iWave Website http://www.iwavesystems.com/product/video-deinterlacer/
Or from our Local Partners in your region http://www.iwavesystems.com/about-us/business-partner.html

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