

iW- Programmable Timer used for timing control applications in microcomputer systems. The design is capable of generating accurate time delays under software control. Three independent 16-bit counters are supported which can be configured to operate in one of the six operating modes supported

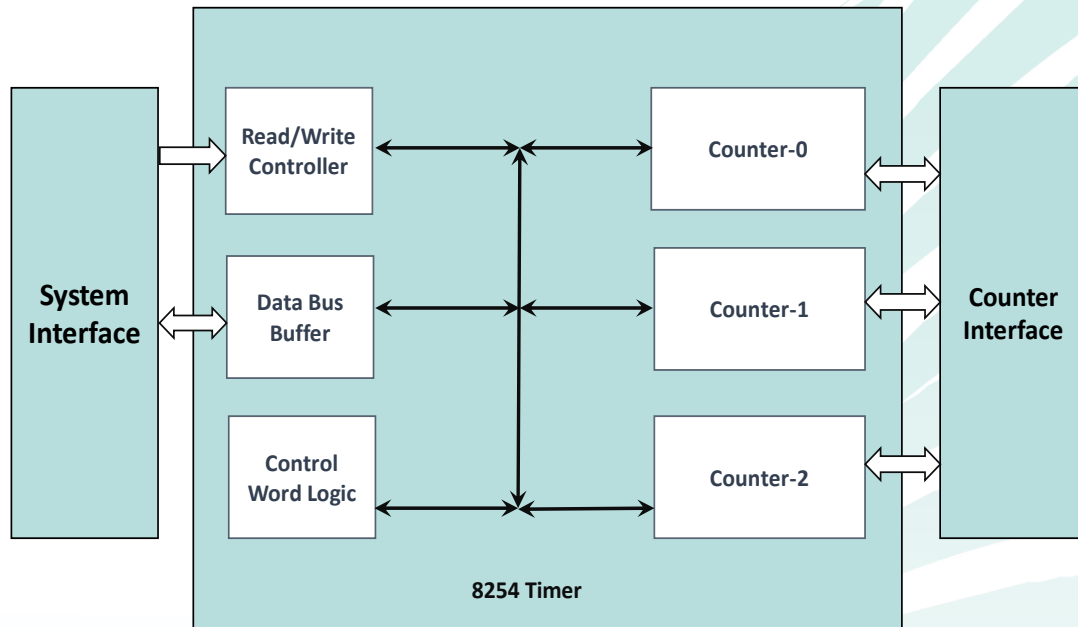
Highlights

- Compatible to 8254 and uPD71054
- Timers can be configured to operate in one of the six operating modes supported.

Features

- Three independently operated 16-bit counters
- Binary/BCD count operation
- Multiple Latch command for easy monitoring
- Counter Latch command
- Six count modes available for each counter
 - o Interrupt at the End of Count
 - o GATE Re-triggerable One-Shot
 - o Rate Generator
 - o Square Wave Generator
 - o Software-Triggered Strobe
 - o Re-triggerable Hardware-Triggered Strobe
- Functionally based on 8254

iW 8254 Timer IP block diagram



Deliverables

- RTL source code or Netlist
- IP example design
- IP datasheet
- Integration Manual

Licensing Options

- Non-Transferable: Single Project/Product Netlist License – Single Site or Multi Site
- Non-Transferable: Multi Project /Product Netlist License – Single Site or Multi Site
- Non-Transferable: Single Project/Product RTL Source Code License – Single Site or Multi Site
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Technical Support

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- The Client may open a new support incident by emailing to a technical support engineer
- iWave's response time shall be within 24 hours of the initial call, with the details of the action plan to resolve
- Support assistance shall be delivered by telephone, email and/or remote assistance via a web meeting
- iWave shall provide remote debugging support irrespective of the time zone/ region

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iW- 8254 Timer FPGA IP

The IP can be ordered online from the iWave Website <http://www.iwavesystems.com/product/8254-programmable-timer>

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