

## SD/SDIO Host controller 3.0 Resource Utilization

### Xilinx:

Device Family	LUTs	Registers	BRAM
ZYNQ Ultrascale +	2574	2321	2
ZYNQ 7000	2625	2315	2

### Intel:

Device Family	ALM	Registers	RAM Blocks
Stratix V	2256	2305	3
Cyclone V	2551	2170	3
Arria 10	2274	2308	3

### Lattice:

Device Family	LUT4s	Registers	Block RAMs
ECP5U	3253	2514	2

### Microchip:

Device Family	4LUT	DFF	RAM1K20/ RAM1K18
Smart fusion 2	3409	2987	2
Polar fire	3545	3063	2