

Video De-Interlacer Resource Utilization

Xilinx:

Device Family	LUTs	Registers	BRAM
Spartan 6	123	67	2

Intel:

Device Family	Logical elements Combinational	Logical Elements registers
Cyclone IV E	288	192

Microchip:

Device Family	Core SEQ	Core COMB
ProASIC3	192	502

Lattice:

Device Family	LUTs	Registers
XP2	328	191