3 Resource Utilization

The table below shows the resource utilization summary of ARINC 818-2 IP Core in Xilinx and Altera FPGAs.

Table 8: Resource Utilization in Xilinx KC705

RESOURCE	KC705		
	UTILIZATION	AVAILABLE	UTILIZATION %
LUT	1302	203800	0.64
LUTRAM	2	64000	0.003125
FF	1308	407600	0.32
BRAM	4	445	0.9
DSP	1	840	0.12

Table 9: Resource Utilization in Xilinx AC701

RESOURCE	AC701		
	UTILIZATION	AVAILABLE	UTILIZATION %
LUT	1305	134600	0.97
LUTRAM	2	46200	0.0043290043
FF	1308	269200	0.49
BRAM	4	365	1.1
DSP	1	740	0.14

Table 10: Resource Utilization in Xilinx KCU105

RESOURCE	KCU105		
	UTILIZATION	AVAILABLE	UTILIZATION %
LUT	1314	242400	0.54
LUTRAM	2	112800	0.0017730497
FF	1282	484800	0.26
BRAM	4	600	0.67
DSP	1	1920	0.05

Table 11: Resource Utilization in Xilinx ZC706

	ZC706		
RESOURCE	**UTILIZATION**	**AVAILABLE**	**UTILIZATION %**
LUT	1303	218600	0.6
LUTRAM	2	70400	0.0028409092
FF	1308	437200	0.3
BRAM	4	545	0.73
DSP	1	900	0.11

Table 12: Resource Utilization in Altera Cyclone V

	Cyclone V		
RESOURCE	**UTILIZATION**	**AVAILABLE**	**UTILIZATION %**
Combinational ALUTs	1194	-	-
ALMs	724	56480	0.0128
LABs	96	5648	0.017
Logic Registers	834	225920	0.73
Memory Bits	278528	14049280	0.02

Table 13: Resource Utilization in Altera Arria 10

	Arria 10		
RESOURCE	**UTILIZATION**	**AVAILABLE**	**UTILIZATION %**
Combinational ALUTs	1,193	-	-
ALMs	746	427,200	0.0017
LABs	103	42,720	0.0024
Logic Registers	814	1708800	0.0004
Memory Bits	299008	111124480	0.002